

WHAT IS CLAIMED IS:

1. A radio frequency tuner for analogically and digitally modulated signals, comprising a tuner arrangement for converting any selected one of analog and digital channels to one of baseband and zero intermediate frequency analog quadrature signals, a dual analog-digital converter for converting said analog quadrature signals to digital quadrature signals, and a digital signal processor for processing said digital quadrature signals, said processing including performing channel filtering to pass said selected channel and substantially to reject other channel signals.
2. A tuner as claimed in claim 1, comprising a remodulator for converting an analog channel processed by said processor to an analogically modulated signal at a predetermined intermediate frequency.
3. A tuner as claimed in claim 2, in which said predetermined intermediate frequency is a non-zero intermediate frequency.
4. A tuner as claimed in claim 2, in which said remodulator comprises a digital remodulator and a digital-analog converter.
5. A tuner as claimed in claim 1, in which said processor is arranged to correct quadrature conversion errors in said tuner arrangement.
6. A tuner as claimed in claim 1, comprising a dual analog anti-alias filter between said tuner arrangement and said analog-digital converter.
7. A tuner as claimed in claim 6, in which said anti-alias filter is a low pass filter.
8. A tuner as claimed in claim 6, in which said dual anti-alias filter is the only filtering between said tuner arrangement and said analog-digital converter.

9. A tuner as claimed in claim 1, in which said processor has selectable filtering characteristics for different modulation standards.
10. A tuner as claimed in claim 1, comprising a digital demodulator connected to said processor.
11. A tuner as claimed in claim 1, comprising an analog demodulator.
12. A tuner as claimed in claim 1, comprising a single monolithic integrated circuit.